Fișa de verificare a standardelor minimale necesare şi obligatorii pentru conferirea titlurilor didactice din învăţământul superior
COMISIA INGINERIE ELECTRONICĂ, TELECOMUNICAȚII ȘI NANOTEHNOLOGIE

Conferentiar universitar dr. ing. Stefan ONIGA

Nr. Crt	Domeniul activitatilor	Tipul activitatilor	Categorii si restrictii	Subcategorii	Indicatori (kpi)	Numar	Punctaj
1	Activitatea didactică şi profesională (A1)	1.1 Cărţi şi capitole în cărţi de specialitate în edituri recunoscute	1.1.1 Cărți / Monografii / Capitole ca autor	1.1.1.1 Internationale	25	1	25
				1.1.1.2 Nationale	20	4	80
		1.2 Material didactic / Lucrări didactice	1.2.1 Manuale didactice		10	3	30
					Total punctaj (A1)		135
2	Activitatea de cercetare (A2)	2.1 Articole în reviste cotate şi în volumele unor manifestări ştiinţifice indexate ISI proceedings			(25 + 20 * factor impact)/ nr. de autori	28	281,90
		2.2 Articole în reviste şi volumele unor manifestări ştiinţifice indexate în alte baze de date internaţionale			20/nr. de autori	32	259,19
		2.3 Proprietate intelectuală, brevete de invenție, certificate ORDA		2.3.1.1 internaţionale	35/nr. de autori	0	0,00
				2.3.1.2 naţionale	25/nr. De autori	0	0,00
		2.4 Granturi / proiecte câștigate prin competiție	2.4.1 Director / Responsabil	2.4.1.1 Internationale	20*ani de desfăşurare	4	83,20
				2.4.1.2 Nationale	10*ani de desfăşurare	3	30,00
			2.4.2 Membru echipă	2.4.2.1 Internaționale	4*ani de desfăşurare	2	18,00
				2.4.2.2 Nationale	2*ani de desfăşurare	6	26,00
					Total punctaj (A2)		698,29
3	Recunoaşterea impactului activ (A3)	3.1 Citări în cărți, reviste şi volume ale unor manifestări ştiinţifice		3.1.1 Cărți, ISI	8 / nr aut art. Citat	52	119,96
				3.1.2 BDI	4 / nr aut art. citat	79	97,27
		3.2 Prezentări invitate în plenul unor manifestări ştiinţifice naţionale şi	Punctaj unic pentru fiecare activitate	3.2.1 Internationale	10	0	0
				3.2.2 Nationale	5	0	0
		3.3 Membru în colectivele de redacţie sau comitete ştiinţifice ale revistelor şi manifestărilor ştiinţifice, organizator de manifestări ştiinţifice, internaționale indexate	Punctaj unic pentru fiecare activitate	3.3.1 ISI	10	4	40
				3.3.2 BDI	6	26	156
				3.3.3 naţionale şi internaţionale neindexate	3	10	30
		3.4 Premii în domeniu		*3.4.1 Academia Română, ASAS, AOSR, academii de ramură şi CNCS, premii internaţionale*	*15*	0	30
				3.4.2 Premii naţionale în domeniu	*5*	0	5
					Total punctaj (A3)		478,23
					TOTAL		1311,51

Fişa de verificare a indeplinirii standardelor minimale CNATDCU

Conferentiar universitar dr. ing. Stefan ONIGA

Conditii minimale (Ai)				
Nr.	**Domeniu de activitate (A)**	**Necesar**	**Realizat**	**Indeplinit**
A1	Activitatea didactică şi profesională (A1)	**100**	**135,00**	**DA**
A2	Activitatea de cercetare (A2)	**500**	**698,29**	**DA**
A3	Recunoaşterea impactului activ (A3)	**100**	**478,23**	**DA**
TOTAL (A)		**700**	**1311,51**	**DA**

Conditii minimale obligatorii pe subcategorii		**Necesar**	**Realizat**	**Indeplinit**
A1.1.1.-A1.1.2	**Carti si capitole in carti de specialitate**	**4**	**5**	**DA**
A1.2.1	Material didactic / Lucrari didactice	**2**	**3**	**DA**
A2.1.	Articole in reviste cotate si in volumele unor manifestari stiintifice indexate ISI proceedings	**12**	**28**	**DA**
A2.4.1	Granturi/proiecte castigate prin competitie (Director/ responsabil)	**2**	**7**	**DA**
A3.1.1-A3.1.2	Numar de citari in carti, reviste si volume ale unor manifestari stiintifice ISI sau BDI	**20**	**131**	**DA**
	Factor de impact cumulat pentru publicatii	**6**	**8,361**	**DA**

Nr. Crt	Domeniul activitatilor	Tipul activitatilor	Categorii si restrictii		Indicatori (kpi)	Numar	Punctaj
1	Activitatea didactică şi profesională (A1)	1.1 Cărţi şi capitole în cărţi de specialitate în edituri recunoscute	1.1.1 Cărţi / Monografii / Capitole ca autor	1.1.1.1 Internationale	25	1	25
				1.1.1.2 Nationale	20	4	80
		1.2 Material didactic / Lucrări didactice	1.2.1 Manuale didactice		10	3	30
					TOTAL A1		135

1.1.1 Cărţi / Monografii / Capitole ca autor
1.1.1.1 Internationale

Nr.	Autori	Titlu capitol / carte	Editura	ISBN	An aparitie	Numar pagini	Numar autori	Punctaj
1	S. Oniga, P. Pop-Sitar	Application Possibilities of Hardware Implemented Hybrid Neural Networks to Support Independent Life of Elderly People, Lecture Notes in Computer Science	Science, Springer-Verlag	9783642408458	2013	10	2	25
							Total	**25**

A 1.1.1.2 Nationale

Nr.	Autori	Titlu capitol / carte	Editura	ISBN	An aparitie	Numar pagini	Numar autori	Punctaj
1	Claudiu Lung, Ștefan Oniga, Radu Joian, Ciprian Gavrincea	Circuite integrate digitale	Editura Universității de Nord	978-973-1729-86-2	2008	120	4	20
2	Gavrincea Ciprian Oniga Ștefan, Radu Joian	Sisteme și circuite digitale	Risoprint	913-751-273-1	2006	146	3	20
3	Oniga Ștefan	Circuite digitale	Risoprint	973-656-244-1	2002	171	1	20
4	Mic Daniel Oniga Ștefan	Proiectare asistată cu circuite logice programabile	Risoprint	973-656-246-8	2002	168	2	20
							Total	**80**

1.2 Material didactic / Lucrări didactice
1.2.1 Suport de curs inclusiv electronic

Nr.	Autori	Curs		An aparitie	Numar pagini	Numar autori	Punctaj
1	Oniga Stefan	Cirrcuite digitale - Suport curs, seminar	https://sites.google.com/site/circuitedigitale2012/home/materiale-suport	2012	223	1	10
2	Oniga Stefan	Sisteme cu circuite integrate digitale - Suport curs, seminar	http://ece.ubm.ro/ea/cursuri/SCID/SCIDcurs.htm	2013	234	1	10
3	Oniga Stefan	Microcontrolere - Indrumator laborator, suport curs	https://sites.google.com/site/microcontrolereunbm/home/materiale-suport	2013	140	1	10
						Total	**30**

Nr. Crt	Domeniul activitatilor	Tipul activitatilor	Categorii si restrictii	Subcategorii	Indicatori (kpi)	Numar	Punctaj
2	**Activitatea de cercetare (A2)**	2.1 Articole în reviste cotate şi în volumele unor manifestări ştiinţifice indexate ISI proceedings			(25 + 20 * factor impact)/ nr. de autori	28	281,90
		2.2 Articole în reviste şi volumele unor manifestări ştiinţifice indexate în alte baze de date internaţionale			20/nr. de autori	32	259,19
		2.3 Proprietate intelectuală, brevete de invenţie, certificate ORDA		2.3.1.1 internaţionale	35/nr. de autori	0	0,00
				2.3.1.2 naţionale	25/nr. De autori	0	0,00
		2.4 Granturi / proiecte câştigate prin competiţie	2.4.1 Director / Responsabil	2.4.1.1 Internationale	20*ani de desfăşurare	4	83,20
				2.4.1.2 Nationale	10*ani de desfăşurare	3	30,00
			2.4.2 Membru echipă	2.4.2.1 Internaţionale	4*ani de desfăşurare	2	18,00
				2.4.2.2 Nationale	2*ani de desfăşurare	6	26,00
					TOTAL		698,29

2.1 Articole în reviste cotate şi în volumele unor manifestări ştiinţifice indexate ISI proceedings

Nr.	Autori	Titlu lucrare, Brevet / Revista (Conferinta), vol. iss, pp.	Factor de impact	Nr. Autori	Punctaj	Numar
1	S. Oniga, J. Suto	Activity recognition in adaptive assistive systems using artificial, neural networks. Elektronika ir Elektroteknika, Vol 22 No.1, 2016. pp. 68-72	0,561	2	18,11	1
2	S. Oniga, J. Suto	Optimal Recognition Method of Human Activities Using Artificial Neural Networks, Measurment Science Review, Volume 15, No. 6, 2015	0,989	2	22,39	1
3	R. Besenczi, M. Szilágyi, N. Bátfai, A. Mamenyák, S. Oniga, M. Ispány	Using Crowdsensed Information for Traffic Simulation in the Robocar World Championship Framework, 6th IEEE Conference on Cognitive Infocommunications, CogInfoCom 2015 Győr, Hungary, pp. 333-337, 2015	0,25	6	5,00	1
4	I. Orha, S. Oniga	Activity recognition using an e-Textile data acquisition system, 2015 IEEE 21st International Symposium for Design and Technology in Electronic Packaging (SIITME), pp. 335-339, 2015	0,25	2	15,00	1
5	J. Suto, S. Oniga	A New Relation between “Twiddle Factors” in the Fast Fourier Transformation, Elektronika ir Elektrotechnika, Vol 21, No 4 (2015), pp, 56-59, 2015	0,561	2	18,11	1
6	I. Orha, S. Oniga	Study regarding the optimal sensors placement on the body for human activity recognition, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging (SIITME), pp, 203-206 2014.	0,25	2	15,00	1
7	S. Sebastian, S. Oniga, C. Lung	Magnetic sensors in inertial navigation system, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging (SIITME), pp. 211-214, 2014,	0,25	3	10,00	1
8	J. Suto, S. Oniga, G. Hegyesi	A simple fast fourier transformation algorithm to microcontrollers and mini computers, 18th International Conference on Intelligent Engineering Systems (INES), July 3-5, 2014, Tihany, Hungary, pp. 61-65, 2014	0,25	3	10,00	1

9	S. Oniga, J. Suto	Human activity recognition using neural networks, in Proceedings of the 2014 15th International Carpathian Control Conference, ICCC 2014, pp. 403-406, 2014.	0,25	2	15,00	1
10	G. Sebestyen, A. Hangan, S. Oniga, Z. Gal	eHealth Solutions in the Context of Internet of Things, in 2014 IEEE INTERNATIONAL CONFERENCE ON AUTOMATION, QUALITY AND TESTING, ROBOTICS, pp, 261-267, 2014.	0,25	4	7,50	1
11	J. Suto, S. Oniga	FPGA implemented reduced Ethernet MAC, in 4th IEEE International Conference on Cognitive Infocommunications, CogInfoCom 2013 - Proceedings, pp. 29-32, 2013.	0,25	2	15,00	1
12	I. Orha, S. Oniga	Automated system for evaluating health status, in 2013 IEEE 19TH INTERNATIONAL SYMPOSIUM FOR DESIGN AND TECHNOLOGY IN ELECTRONIC PACKAGING (SIITME), pp. 219-222, 2013.	0,25	2	15,00	1
13	J. Suto, S. Oniga, I. Orha	Microcontroller based health monitoring system, in 2013 IEEE 19TH INTERNATIONAL SYMPOSIUM FOR DESIGN AND TECHNOLOGY IN ELECTRONIC PACKAGING (SIITME), pp. 227-230, 2013.	0,25	3	10,00	1
14	A. Tisan, M. Cirstea, A. Buchman, A. Parera, S. Oniga, D. Ilea	Holistic Modeling, Design and Optimal Digital Control of a Combined Renewable Power System, in IEEE INTERNATIONAL SYMPOSIUM ON INDUSTRIAL ELECTRONICS (ISIE 2010), pp. 2733-2738, 2010.	0,25	6	5,00	1
15	A. Tisan, M. Cirstea, S. Oniga, A. Buchman	Artificial olfaction system with hardware on-chip learning neural networks, in OPTIM 2010: PROCEEDINGS OF THE 12TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, PTS I-IV, pp. 884-889, 2010.	0,25	4	7,50	1
16	S. Oniga, A. Tisan, C. Lung, A. Buchman, I. Orha	Adaptive Hardware-Software Co-Design Platform for Fast Prototyping of Embedded Systems, in OPTIM 2010: PROCEEDINGS OF THE 12TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, PTS I-IV, pp. 1004-1009, 2010.	0,25	5	6,00	1
17	S. Oniga, A. Tisan, D. Mic, C. Lung, I. Orha, A. Buchman, A. Vida-Ratiu	FPGA Implementation of Feed-Forward Neural Networks for Smart Devices Development, in ISSCS 2009: INTERNATIONAL SYMPOSIUM ON SIGNALS, CIRCUITS AND SYSTEMS, VOLS 1 AND 2, PROCEEDINGS,, pp. 401-404, 2009.	0,25	7	4,29	1
18	D. Mic, A. Tisan, S. Oniga, C. Lung, S. Sabau	The Development of a Simulink Library with FPGA Compatible Parametric Components for Electric Machines Control, in ISSCS 2009: INTERNATIONAL SYMPOSIUM ON SIGNALS, CIRCUITS AND SYSTEMS, VOLS 1 AND 2, PROCEEDINGS,, pp. 561-564, 2009.	0,25	5	6,00	1

19	A. Buchman, S. Lungu, S. Oniga, A. Tisan	Ultrasonic Echo Detection: Experiments Using Large Beam Angle Transducers in Narrow Tubes, in 31ST INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY: RELIABILITY AND LIFE-TIME PREDICTION, pp. 111-116, 2008.	0,25	4	7,50	1
20	D. Mic, S. Oniga, E. Micu, C. Lung	Complete hardware/software solution for implementing the control of the electrical machines with programmable logic circuits, in PROCEEDINGS OF THE 11TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT (OPTIM), VOL III, pp. 107-114, 2008.	0,25	4	7,50	1
21	S. Oniga, A. Tisan, D. Mic, A. Buchman, A. Vida-Ratiu	Optimizing FPGA implementation of Feed-Forward Neural Networks, in PROCEEDINGS OF THE 11TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, VOL IV, pp. 31-36, 2008.	0,25	5	6,00	1
22	A. Tisan, S. Oniga, C. Gavrincea, A. Buchman	FPGA implementation of a self-organized map with on-chip learning, in PROCEEDINGS OF THE 11TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, VOL IV, pp. 81-86, 2008.	0,25	4	7,50	1
23	GC Gavrincea, A. Tisan, A. Buchman, S. Oniga	Survey of wavelet based denoising filter design, in 2007 30TH INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY, pp. 112-116, 2007.	0,25	4	7,50	1
24	A. Buchman, A. Tisan, S. Oniga	Ultrasonic Data Acquisition: Signal to noise ratio improvement, in 2007 30TH INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY, pp. 393-398, 2007.	0,25	3	10,00	1
25	S. Oniga, A. Tisan, D. Mic, A. Buchman, A. Vida-Ratiu	Hand postures recognition system using artificial neural networks implemented in FPGA, in 2007 30TH INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY, pp. 507-512, 2007.	0,25	5	6,00	1
26	A. Tisan, A. Buchman, S. Oniga, C. Gavrincea	A generic control block for feedforward neural network with on-chip delta rule learning algorithm, in 2007 30TH INTERNATIONAL SPRING SEMINAR ON ELECTRONICS TECHNOLOGY, pp. 567-570, 2007.	0,25	4	7,50	1
27	A. Tisan, S. Oniga, B. Attila, G. Ciprian	Architecture and algorithms for syntetizable neural networks with on-chip learning, in International Symposium on Signals, Circuits and Systems, ISSCS 2007, pp. 265-268, 2007.	0,25	4	7,50	1
28	D. Mic, E. Micu, S. Oniga	Hardware and software co-design method for implementation of closed loop control for a brushless DC motor, in PROCEEDINGS OF THE 10TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, VOL III: INDUSTRIAL AUTOMATION AND CONTROL, pp. 59-66, 2006.	0,25	3	10,00	1
		FI cumulat	**8,361**	**Total**	**281,90**	**28**

2.2 Articole în reviste şi volumele unor manifestări ştiinţifice indexate în alte baze de date internaţionale (BDI)

Nr.	Autori	Titlu lucrare / Revista (Conferinta), vol. iss, pp.	Baza de date	Nr. Autori	Punctaj	Numar
1	J. Suto, S. Oniga, A. Buchman	Real time human activity monitoring, Annales Mathematicae et Informaticae, Vol.44, pp, 187-196, 2015.	Scopus	2	10,00	1
2	A.I. Alexan, A.R. Osan, S. Oniga	Personal assistant robot, in 2012 IEEE 18th International Symposium for Design and Technology of Electronics Packages, SIITME 2012 - Conference Proceedings, pp. 69-72, 2012.	Scopus, IEEE Xplore	3	6,67	1
3	I. Orha, S. Oniga	Current distortions compensation method for frequency converters, in 2012 IEEE 18th International Symposium for Design and Technology of Electronics Packages, SIITME 2012 - Conference Proceedings, pp. 105-109, 2012.	Scopus, IEEE Xplore	2	10,00	1
4	S. Oniga, J. Vegh, I. Orha	Intelligent human-machine interface using hand gestures recognition, in 2012 IEEE International Conference on Automation, Quality and Testing, Robotics, AQTR 2012 - Proceedings, pp. 559-563, 2012.	Scopus, IEEE Xplore	3	6,67	1
5	J. Suto, A. Mate, J. Vegh, I. Oniga	Developing a general purpose data collector framework for robots, in Proceedings of the 2012 13th International Carpathian Control Conference, ICCC 2012, pp. 690-693, 2012.	Scopus, IEEE Xplore	4	5,00	1
6	A. Buchman, C. Lung, M. Horgos, A. Tisan, S. Oniga	Evaluation of load generated harmonics on low voltage networks, in Proceedings of the International Conference on Energy and Environment Technologies and EquipmProceedings of the International Conference on Energy and Environment Technologies and Equipment, EEETE '10, pp. 46-50, 2010.	Scopus	5	4,00	1
7	I. Orha, S. Oniga	Wearable sensors network for activity recognition using inertial sensors, Carpathian Journal of Electronic and Computer Engineering, ISSN:1844-9689, Volume 8, Number 2, 2015, pp. 3-6	Google Scholar, EBSCO, DOAJ, PROQUEST, Index Copernicus	2	10,00	1
8	Z. Gal, B. Almasi, T. Daboczi, . Vida, S. Oniga, S. Baran, I. Farkas	Internet of Things: Application areas and Research Results of the FIRST Project, in Infocommunications Journal, vol. 6, no. 3, pp. 37-44-2014.	Inspec, Compendex, Scopus	7	2,86	1
9	I. Orha, S. Oniga	Wearable sensors network for health monitoring using e-Health platform., in Carpathian Journal of Electronic & Computer Engineering, vol. 7, no. 1, 2014.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	2	10,00	1
10	S. Oniga	ICT Tools for Smart Homes and Assisted Living for Elders, in Advances in Wireless Sensor Networks 2013, Conference Proceedings, pp. 41-46-2013.	Google Scholar	1	20,00	1
11	C. Lung, S. Oniga, A. Buchman, A. Tisan	Wireless data acquisition system for IoT applications, in Carpathian Journal of Electronic and Computer Engineering, vol. 6, no. 1, pp. 64-67-2013.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	4	5,00	1

12	Jozsef Suto, Stefan Oniga	A new C++ implemented feed forward neural network simulator, in Carpathian Journal of Electronic and Computer Engineering, vol. 6, no. 2, pp. 3-6-2013.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	2	10,00	1
13	Al Alexan, AR Osan, S. Oniga	Advanced Medication Dispenser, in Carpathian Journal of Electronic and Computer Engineering, vol. 6, no. 2, pp. 26-31-2013.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	3	6,67	1
14	Jozsef Suto, STEFAN ONIGA	Testing artificial neural network for hand gesture recognition, Creative Mathematics and Informatics, Vol. 22 (2013), No. 2, 15 December 2013, pp. 223-228, 2013.	Google Scholar, Mathematical Reviews, Zentralblatt MATH	2	10,00	1
15	Al Alexan, AR Osan, S. Oniga	AssistMe robot, an assistance robotic platform, in Carpathian Journal of Electronic and Computer Engineering ISSN 1844 - 9689, vol. 5, no. 1, pp. 1-4-2012.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	3	6,67	1
16	S. Oniga I. Orha	Assistance and telepresence robots: a solution for elderly people, in Carpathian Journal of Electronic and Computer Engineering, vol. 5, no. 1, pp. 87-90-2012.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	1	20,00	1
17	S. Oniga J. Suto	Remote controlled data collector robot, in Carpathian Journal of Electronic and Computer Engineering, vol. 5, no. 1, pp. 117-120-2012.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	2	10,00	1
18	Stefan Oniga, Osan Anca Roxana, Alexan Alexandru Iulian	Alternative control method of the smart house: natural gestures, in Carpathian Journal of Electronic and Computer Engineering, vol. 4, no. 1-2011, pp. 97-100-2011.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	3	6,67	1
19	S. Oniga, I. Orha	Hardware Implemented Neural Networks used for Hand Gestures Recognition, in Carpathian Journal of Electronic and Computer Engineering, vol. 4, no. 1-2011, pp. 93-96-2011.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	2	10,00	1
20	O. Chiver, L. Neamt, M. Horgos, S. Oniga, A. Buchman	The Study of Transient Regimes for a Shell-Type Transformer, Carpathian Journal of Electronic and Computer Engineering, Volume 4, Number 1 - 2011, pp.23-26, 2011.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	5	4,00	1
21	Alin Tisan, Stefan Oniga, Daniel Mic, Attila Buchman	Digital implementation of the sigmoid function for FPGA circuits, in ACTA TECHNICA NAPOCENSIS Electronics and Telecommunications, vol. 50, no. 2, pp. 6-2009.	Google Scholar, EBSCO, PROQUEST, Index Copernicus	4	5,00	1
22	D. Mic, S. Oniga	FPGA implementation of a digital tachometer with input Filtering, in International Symposium for Design and Technology of Electronic Packaging, pp. 170-174-2007.	Google Scholar, CiteseerX	2	10,00	1
23	A. Tisan, S. Oniga, A. Buchman, C. Gavrincea	Hardware/software codesign of a pattern recognition system with on-chip learning, in Regional Conference on Embedded and Ambient Systems, RCEAS, pp. 25-26-2007.	Google Scholar, CiteseerX	4	5,00	1
24	A. Tisan, C. Gavrincea, S. Oniga, A. Buchman	Methods for embedded systems design with on-chip learning neural networks, in International symposium for design and technology of electronics packaging, 2007.	Google Scholar, CiteseerX	4	5,00	1

25	S. Oniga, A. Tisan, D. Mic, A. Buchman, C. Gavrincea, A. Vida	Hardware implementation of simple competitive neural networks with layer parallelism, in International Symposium for Design and Technology of Electronic Packages, SIITME, pp. 20-23-2007.	Google Scholar, CiteseerX	6	3,33	1
26	S. Oniga, Alin Tisan, Attila Buchman, Claudiu Lung	Hardware Implementation of Simple Competitive Artificial Neural Networks with Neuron Parallelism, in Proceedings of Regional Conference on Embedded and Ambient Systems, Budapest, Hungary, pp. 27-32-2007.	Google Scholar	4	5,00	1
27	Alin Tisan, Stefan Oniga, Ciprian Gavrincea	Hardware implementation of a MLP network with on-chip learning, in Proceedings of the 5th WSEAS Int. Conf. on Data Networks, Communications & Computers, Bucharest, Romania, pp. 162-167-2006.	Google Scholar, CiteseerX, ACM DL	3	6,67	1
28	S. Oniga	A New Method for FPGA Implementation of Artificial Neural Network Used in Smart Devices, in International Computer Science Conference microCAD, pp. 31-36-2005.	Google Scholar, CiteseerX	1	20,00	1
29	S. Oniga, A. Buchman	A New Method for Hardware Implementation of Artificial Neural Network Used in Smart Sensors, in The 10th International Symposium for Design and Technology of Electronic Packages September, 23-26, Bucharest, Romania, pp. 215-218, 2004.	Google Scholar, CiteseerX	2	10,00	1
30	Stefan Oniga, Virgil Tiponut, Attila Buchman, Daniel Mic	Adaptive Interface L Based on FGA implemented Artificial Neural Network, in Scientific Bul1etin ofthe Politehnica University of Timisoara, Tomul, vol. 49, no. 63, pp. 22-23-2004.	Google Scholar, CiteseerX	4	5,00	1
31	D. Mic, E. Micu, S. Oniga, C. Gavrincea	The FPGA Implementation of a Digital Controller as a Digital Filter, in Scientific Bulletin of the Politehnica University of Timisoara, Tomul 49(63), Fascicola 1, 22-23 octombrie 2004, vol. 49, no. 1, pp. 184-188-2004.	Google Scholar, CiteseerX	4	5,00	1
32	A. Tisan, S. Oniga, C. Gavrincea, D. Mic	A Study Regarding the Implementation with VHDL of a Multiple Clock Gating Scheme for Low Power RTL Design, in Symposium of Electronics and Telecommunications, - Fifth Edition Etc 2002, September 19-20, 2002, Timisoara, Romania, vol. 1, no. 15, pp. 53-56-2002.	Google Scholar, CiteseerX	4	5,00	1
				Total	**259,19**	**32**

A2.4.1.1. Granturi/proiecte castigate prin competitie: director/responsabil de proiect - internationale

Nr.	Membrii (exceptie pers. proprie)	Denumire proiect, tip, cod, date identificare	Perioada	Nr. ani derulare	Punctaj	Numar
1	Yongjiang GUO PhD	"Human activity recognition system using artificial neural networks" - Joint research project with Beijing University of Posts and Telecommunications, School of Science; Competitie internationala: Campus Hungary Higher Education Staff Short Term Mobility, B2/4R/16048; 2015	25.05.2015 - 19.06.2015 (4 saptamani)	0,08	1,6	1
2	Lung Claudiu	Grant 5310-XUP-1-AKLBXH din 10.07.2015, cu compania Xilinx Inc. din SUA. „Dezvoltare infrastructură laboratoare educatiomale în domeniul proiectării cu circuite logice programabile si Laborator cercetare Sisteme dedicate inteligente”.	07.2015	0,08	1,6	1
3		Grant 5310-XUP-26482–4 din 14.04.2004, respectiv contract adițional 5310-XUP-48209-10 din 10.03.2006, cu compania Xilinx Inc. Din SUA. „Dezvoltare infrastructură cercetare în domeniul proiectării cu circuite logice programabile”.	2004-2006	2	40	1
4		Grant XUP-7017-8 din 28.03.2001, respective contract adițional 2511-XUP-19570–1 din 24.03.2003 cu compania Xilinx Inc. Din SUA. „Dotare laborator de cercetare în domeniul proiectării cu circuite logice programabile”.	2001-2003	2	40	1
				Total	**83,2**	**4**

A2.4.1.2. Granturi/proiecte castigate prin competitie: director/responsabil de proiect - nationale

Nr.	Membrii (exceptie pers. proprie)	Denumire proiect, tip, cod, date identificare	Perioada	Nr. ani derulare	Punctaj	Numar
1	Buchman Attila, Costea Cristinel, Vida-Rațiu Andrei, Lung Claudiu, Mic Daniel, etc.	Program de Cercetare de excelență CEEX-M3 PASED – contract nr.: 253/01.08.2006 Dezvoltarea parteneriatelor la nivel național și internațional, in domeniul Sisteme Dedicate, in vederea organizării de manifestări științifice și pregătirea de proiecte comune în programul cadru 7 UE.	2006-2008	2	20	1
2	Mic Danie, Tisan Alin, Gavrincea Ciprian, etc	Nucleu microcontroler (S*****), Soft processor core. Contract cu S.C. Electronics Engineering S.A. Baia Mare 2002	2002	0,5	5	1
3	Mic Danie, Tisan Alin, Gavrincea Ciprian, etc	Power optimizer of FPGA implemented designs, Metoda, Model experimental, Software. Contract cu S.C. Electronics Engineering S.A. Baia Mare 2001	2001	0,5	5	1
				Total	30	3

A2.4.2.1. Granturi/proiecte castigate prin competitie: membru in echipa - internationale

Nr.	Denumire proiect, tip, cod, date identificare		Perioada	Nr. ani derulare	Punctaj	Numar
1	Mihaly Leonard TUTU HLANGANANI	Proiectul de cooperare bilaterală Romania – Africa de Sud. Nr. 82/19.08.2008. ZEMIP	2009-2011	2	8	1
2	University of Debrecen, Inter-University Cooperative Research Centre, Hungarian Academy of Sciences, Institute for Nuclear Research, ...	Future Internet Research, Services and Technology/ Subproject: Internet of Things TÁMOP-4.2.2.C-11/1/KONV-2012-0001 Hungary. Responsabil tema cercetare: ICT tools for smart homes and assisted living for elders	2012-2015	2,5	10	1
				Total	**18**	**2**

A2.4.2.2. Granturi/proiecte castigate prin competitie: membru in echipa - nationale

Nr.	Director	Denumire proiect, tip, cod, date identificare	Perioada	Nr. ani derulare	Punctaj	Numar
1	Peter Anca	Smart functions of packages containing nano-structured materials in food preservation – SMARTPACK ERA-NET-MicroNanoTechnologies (MNT-ERANET)	2012-2015	3	6	1
2	Marian Monica	Monitorizarea acțiunii microbiotei solului in vederea utilizării ei in remedierea ecologica a iazurilor de decantare. CEEX M3 AMSREI nr. 1822/18.09.2007	2008-2010	2	4	1
3	Varga Camelia	Bioacumularea metalelor grele in lanțul sol legume – om. Programul 4 "Parteneriate in domenii prioritare" BIOMEG nr. 521441 01.10.2008.	2008-2011	2	4	1
4	Mihaly Leonard	Reabilitarea iazurilor de decantare prin aplicare de amendamente și cultivarea unor specii vegetale cu adaptabilitate ridicata la conținutul de metale grele. Programul 4 "Parteneriate in domeniile prioritare" RIVAM nr. 32124/2008.	2009-2011	2	4	1
5	Tiponut Virgil	Mediu integrat pentru deplasarea asistată a persoanelor cu handicap vizual. CNCSIS nr. 639/2005	2005-2007	2	4	1
6	Micu Emil	Informatică aplicată Program major banca Mondiala - CNFIS nr. 45.139/2000	2000-2002	2	4	1
				Total	**26**	**6**

Nr. Crt	Domeniul activitatilor	Tipul activitatilor	Categorii si restrictii	Subcategorii	Indicatori (kpi)	Punctaj
3	Recunoaşterea impactului activităţii (A3)	3.1 Citări în cărţi, reviste şi volume ale unor manifestări ştiinţifice		3.1.1 Cărţi, ISI	8 / nr aut art. Citat	119,96
				3.1.2 BDI	4 / nr aut art. citat	97,27
		3.2 Prezentări invitate în plenul unor manifestări ştiinţifice naţionale şi internaţionale şi profesor invitat (exclusiv POS, ERASMUS)	Punctaj unic pentru fiecare activitate	3.2.1 Internationale	10	0
				3.2.2 Nationale	5	0
		3.3 Membru în colectivele de redacţie sau comitete ştiinţifice ale revistelor şi manifestărilor ştiinţifice, organizator de manifestări ştiinţifice, internaţionale indexate ISI	Punctaj unic pentru fiecare activitate	3.3.1 ISI	10	40
				3.3.2 BDI	6	156
				3.3.3 naţionale şi internaţionale neindexate	3	30
		3.4 Premii în domeniu		*3.4.1 Academia Română, ASAS, AOSR, academii de ramură şi CNCS, premii*	*15*	30
				3.4.2 Premii naţionale în domeniu	*5*	5
					TOTAL	478,23

A3.1.1. Citari in carti, reviste si volume ale unor manifestari stiintifice (carti, ISI)

Nr.	Articol citat	Articol care citeaza	Numar autori art.citat	Punctaj
1	Sebestyen, G., Hangan, A., Oniga, S., & Gal, Z. (2014, May). eHealth solutions in the context of Internet of Things. In Automation, Quality and Testing, Robotics, 2014 IEEE International Conference on (pp. 1-6).	Maia, P., Batista, T., Cavalcante, E., Baffa, A., Delicato, F. C., Pires, P. F., & Zomaya, A. (2014). A Web platform for interconnecting body sensors and improving health care. Procedia Computer Science, 40, 135-142.	4	2,00
2	-//-	Man, Lee Carman Ka, Cheng Mei Na, and Ng Chun Kit. "IoT-based Asset Management System for Healthcare-related Industries." International Journal of Engineering Business Management, 7:19 (2015).	4	2,00
3	-//-	Srivastava, S., Pant, M., Abraham, A., & Agrawal, N. (2015). The technological growth in eHealth services. Computational and mathematical methods in medicine, 2015.	4	2,00
4	Gál, Z., Almási, B., Dabóczi, T., Vida, R., Oniga, S., Baran, S., & Farkas, I. Internet of Things Application areas and Research Results of the FIRST Project. Infocommunications Journal, 6(3), 37-44. 2014	Baranyi, Péter, Adam Csapo, and Gyula Sallai. Cognitive Infocommunications (CogInfoCom). Springer, 2015. (Carte)	7	1,14
5	-//-	Baranyi, Péter, Adam Csapo, and Gyula Sallai. "Cognitive Capabilities in the Future Internet." Cognitive Infocommunications. Springer International Publishing, 2015. 173-185.	7	1,14
6	I. Orha, S. Oniga 'Automated system for evaluating health status, in 2013 IEEE 19TH INTERNATIONAL SYMPOSIUM FOR DESIGN AND TECHNOLOGY IN ELECTRONIC PACKAGING (SIITME), pp. 219-222, 2013.	Santos, Olga C., and Jesus G. Boticario. Exploring Arduino for building educational context-aware recommender systems that deliver affective recommendations in social ubiquitous networking environments. Web-Age Information Management. Springer International Publishing, 2014. 272-286.	2	4,00

7	AI Alexan, AR Osan, S. Oniga, 'Advanced Medication Dispenser, in Carpathian Journal of Electronic and Computer Engineering, vol. 6, no. 2, pp. 26-31-2013.	Shan, T., Sun, Z., Zhang, W., & Chen, Q. (2015). A Novel Coaxial Stack System for Petri Dish Dispenser. In Intelligent Robotics and Applications (pp. 511-522). Springer International Publishing. 2015	3	2,67
8	C Lung, S Oniga, A Buchman, A Tisan, Wireless data acquisition system for IoT applications, CARPATHIAN JOURNAL OF ELECTRONIC AND COMPUTER ENGINEERING 6:(1) pp. 64-67. (2013)	Santos, Olga C., and Jesus G. Boticario. Exploring Arduino for building educational context-aware recommender systems that deliver affective recommendations in social ubiquitous networking environments. Web-Age Information Management. Springer International Publishing, 2014. 272-286.	4	2,00
9	S. Oniga, J. Vegh, I. Orha, 'Intelligent human-machine interface using hand gestures recognition, in 2012 IEEE International Conference on Automation, Quality and Testing, Robotics, AQTR 2012 - Proceedings, pp. 559-563, 2012.	Lung C, Buchman A, Multifunctional communication system controller implemented in FPGA, In: Design and Technology in Electronic Packaging (SIITME), 2013 IEEE 19th International Symposium for. Galati, România: 2013IEEE, (2013.) , pp. 211-214. IEEE. 2013	3	2,67
10	Orha Ioan, Oniga Stefan, Assistance and telepresence robots: A solution for elderly people, CARPATHIAN JOURNAL OF ELECTRONIC AND COMPUTER ENGINEERING 5:(1) pp. 87-90. (2012)	Lung C, Buchman A, Multifunctional communication system controller implemented in FPGA, In: Design and Technology in Electronic Packaging (SIITME), 2013 IEEE 19th International Symposium for. Galati, România: 2013IEEE, (2013.) , pp. 211-214. (IEEE)	2	4,00
11	-//-	Lung, C., S. Sabou, and A. Buchman. "Emergency radio communication network controller implemented in FPGA." Design and Technology in Electronic Packaging (SIITME), 2014 IEEE 20th International Symposium for. IEEE, 2014.	2	4,00
12	Oşan Anca Roxana, Alexan Alexandru I, Oniga Stefan, AssistMe robot, an assistance robotic platform, CARPATHIAN JOURNAL OF ELECTRONIC AND COMPUTER ENGINEERING 5:(1) p. 1-4.. (2012)	Lung C, Buchman A, Multifunctional communication system controller implemented in FPGA, In: Design and Technology in Electronic Packaging (SIITME), 2013 IEEE 19th International Symposium for. Galati, România: 2013IEEE, (2013.) , pp. 211-214. (IEEE)	3	2,67
13	-//-	Lung, C., S. Sabou, and A. Buchman. "Emergency radio communication network controller implemented in FPGA." Design and Technology in Electronic Packaging (SIITME), 2014 IEEE 20th International Symposium for. IEEE, 2014.	3	2,67
14	A.I. Alexan, A.R. Osan, S. Oniga, Personal assistant robot 2012 IEEE 18th International Symposium for Design and Technology in Electronic Packaging (SIITME).Alba Iulia, România, 2012 . pp. 69-72.	Lung C, Buchman A, Multifunctional communication system controller implemented in FPGA, In: Design and Technology in Electronic Packaging (SIITME), 2013 IEEE 19th International Symposium for. Galati, România, 2013 pp. 211-214. (IEEE) 2013	3	2,67
15	-//-	Lung, C., S. Sabou, and A. Buchman. "Emergency radio communication network controller implemented in FPGA." Design and Technology in Electronic Packaging (SIITME), 2014 IEEE 20th International Symposium for. IEEE, 2014.	3	2,67
16	-//-	Bedaf, S., Gelderblom, G. J., & De Witte, L.. Overview and Categorization of Robots Supporting Independent Living of Elderly People: What Activities Do They Support and How Far Have They Developed. Assistive Technology, 27(2), 88-100. 2015	3	2,67

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18	-//-	Lung, C., S. Sabou, and A. Buchman. "Emergency radio communication network controller implemented in FPGA." Design and Technology in Electronic Packaging (SIITME), 2014 IEEE 20th International Symposium for. IEEE, 2014.	4	2,00
19	S. Oniga J. Suto, Remote controlled data collector robot, CARPATHIAN JOURNAL OF ELECTRONIC AND COMPUTER ENGINEERING 5:(1) pp. 117-120. (2012)	Lung C, Buchman A, Multifunctional communication system controller implemented in FPGA, In: Design and Technology in Electronic Packaging (SIITME), 2013 IEEE 19th International Symposium for. Galati, România: 2013IEEE, (2013.) , pp. 211-214.	2	4,00
20	-//-	Lamár, K., & Zalotay, P. (2015). Microcontroller implementation of lookup table-based control functions with special emphasis on sequential control according to IEC 61131-3. International Journal of Electrical Engineering Education, 52(2), 111-130.	2	4,00
21	-//-	Lung, C., S. Sabou, and A. Buchman. "Emergency radio communication network controller implemented in FPGA." Design and Technology in Electronic Packaging (SIITME), 2014 IEEE 20th International Symposium for. IEEE, 2014.	2	4,00
22	Tisan Alin, Cirstea Marcian, Oniga Stefan, Buchman Attila, Artificial olfaction system with hardware on-chip learning neural networks, PROCEEDINGS OF THE 12TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, PTS I-IV, pp. 884-889, 2010.	Zamanlooy Babak, Mirhassani Mitra, Efficient hardware implementation of threshold neural networks, In: New Circuits and Systems Conference (NEWCAS), 2012 IEEE 10th International. IEEE, 2012. (ISBN 1467308579) pp. 1-4.	4	2,00
23	-//-	Zamanlooy, B., & Mirhassani, M. (2015). CVNS Synapse Multiplier for Robust Neurochips With On-Chip Learning. Very Large Scale Integration (VLSI) Systems, IEEE Transactions on, 23(11), 2540-2551.	4	2,00
24	Oniga Stefan, Tisan Alin, Lung Claudiu, Buchman Attila, Orha Ioan, Adaptive Hardware-Software Co-Design Platform for Fast Prototyping of Embedded Systems, 12th International Conference on Optimization of Electrical and Electronic Equipment, OPTIM 2010, Brasov, pp. 1004-1009.,	Alecsa Bogdan, Onea Alexandru, Hardware-Software Co-Design for BLDC Motor Speed Controller Design, Advanced Materials Research 463: pp. 1256-1259. (2012)	5	1,60
25	Oniga Stefan, Tisan Alin, Mic Daniel, Lung Claudiu, Orha Ioan, Buchman Attila, Vida-Ratiu Andrei, FPGA implementation of feed-forward neural networks for smart devices development, International Symposium on Signals, Circuits, and Systems: ISSCS 2009, pp. 401-404.	Li Xiao Jun, Li Lin, IP core based hardware implementation of multi-layer perceptrons on FPGAs: a parallel approach, Advanced Materials Research 433: pp. 5647-5653. (2012)	7	1,14

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27	-//-	Raj, Arockia A. Bazil, et al. "Intensity feedback-based beam wandering mitigation in free-space optical communication using neural control technique." EURASIP Journal on Wireless Communications and Networking 2014.1 (2014): 1-18.	4	2,00
28	-//-	Raj, A. Arockia Bazil, et al. "Design of Cognitive Decision Making Controller for Autonomous Online Adaptive Beam Steering in Free Space Optical Communication System." Wireless Personal Communications 84.1 (2015): 765-799.	4	2,00
29	-//-	Raj, A. Arockia Bazil. Free Space Optical Communication: System Design, Modeling, Characterization and Dealing with Turbulence. Walter de Gruyter GmbH & Co KG, 2016.	4	2,00
30	-//-	Khodja, Djalal Eddine.; Simard, Stephane; Beguenan, Rachid, Implementation of Optimized Approximate Sigmoid Function on FPGA Circuit to use in ANN for Control and Monitoring. Journal of Control Engineering and Applied Informatics, 17(2), 64-72. 2015	4	2,00
31	-//-	Laudani, A., Lozito, G. M., Fulginei, F. R., & Salvini, A. (2015). On training efficiency and computational costs of a feed forward neural network: a review. Computational intelligence and neuroscience, 2015, 83.	4	2,00
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33	-//-	Pratap, Rana; Subadra, M, Efficient Digital Implementation of The Sigmoidal Function For Artificial Neural Network, Proceedings of International Conference on Light - Optics - Phenomena, Materials , Devices, and Characterization, Book Series: AIP Conference Proceedings Volume: 1391, Calicut, INDIA Date: MAY 23-25, 2011	4	2,00
34	-//-	Dias, Mauricio A., Daniel O. Sales, and Fernando S. Osorio. "Automatic Generation of LUTs for Hardware Neural Networks." Neurocomputing Vol. 180, pp. 108–120 (2015).	4	2,00
35	-//-	Bharati, K. S., & Jhunjhunwala, A. Implementation of machine learning applications on a fixed-point DSP. In Electrical and Computer Engineering (CCECE), 2015 IEEE 28th Canadian Conference on (pp. 1458-1463). IEEE. 2015	4	2,00

36	D. Mic, S. Oniga, E. Micu, C. Lung, Complete hardware/software solution for implementing the control of the electrical machines with programmable logic circuits, in OPTIM 2008: PROCEEDINGS OF THE 11TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, VOL III, pp. 107-114, 2008.	Raihan, Siti Rohani Sheikh; Abd Rahim, Nasrudin, Comparative Analysis of Three-Phase AC-DC Converters Using HIL-Simulation, JOURNAL OF POWER ELECTRONICS Volume: 13 Issue: 1 Pages: 104-112 Published: JAN 2013, ISSN: 1598-2092	4	2,00
37	D. Mic, S. Oniga, E. Micu, C. Lung, Complete hardware/software solution for implementing the control of the electrical machines with programmable logic circuits, in PROCEEDINGS OF THE 11TH INTERNATIONAL CONFERENCE ON OPTIMIZATION OF ELECTRICAL AND ELECTRONIC EQUIPMENT, OPTIM Brasov 2008. VOL III, pp. 107-114.	Vyas, S., Gupte, A., Gill, C. D., Cytron, R. K., Zambreno, J., and Jones, P. H. 2013. Hardware architectural, support for control systems and sensor processing. ACM Trans. Embedd. Comput. Syst. 13, 2, (September 2013), 25 pages. DOI: 10.1145/2514641.2514643	4	2,00
38	Tisan Alin, Oniga Stefan, Gavrincea Ciprian, Buchman Attila, FPGA implementation of a self-organized map with on-chip learning, 11th IEEE International Conference on Optimization of Electrical and Electronic Equipment: OPTIM Brasov 2008. pp. 81-86.	Kolasa, M., & Dlugosz, R. An advanced software model for optimization of self-organizing neural networks oriented on implementation in hardware. In Mixed Design of Integrated Circuits & Systems (MIXDES), 2015 22nd International Conference (pp. 266-271). IEEE. 2015	4	2,00
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40	Oniga Stefan, Tisan Alin, Mic Daniel, Buchman Attila, Vida-Ratiu Andrei, Optimizing FPGA implementation of Feed-Forward Neural Networks, 11th IEEE International Conference on Optimization of Electrical and Electronic Equipment: IEEE OPTIM 2008, pp. 31-36.	Záluský, R., Durackova, D., Stopjaková, V., Nagy, L., & Sedlák, V. Novel architecture of a digital neuron for FFNN employing special multiplication. In ECAI, pp. 933-938. 2014.	5	1,60
41	-//-	Bohrn, PRÁCE Ing Marek. "Promising circuit structures for modular neural networks." Doctoral thesis, Brno 2014	5	1,60
42	Gavrincea GC, Tisan A, Buchman A, Oniga S, Survey of wavelet based denoising filter design, Electronics Technology, 30th International Spring Seminar on. Cluj-Napoca, România, 2007, pp. 112-116.	Lerga J, Sucic V, Sersic D, Image denoising based on the one-dimensional LPA-RICI method, In: Proceedings Elmar - International Symposium Electronics in Marine. Zadar: IEEE, 2009. (ISBN 9789537044107) pp. 57-60. (ELMAR-2009 - 51st International Symposium ELMAR-2009) Paper 5342861.	4	2,00
43	Alin Tisan, Attila Buchman, Oniga Stefan, C Gavrincea, A generic control block for feedforward neural network with on-chip delta rule learning algorithm, 30th International Spring Seminar on Electronics Technology: Emerging Technologies for Electronics Packaging. 2007. pp. 567-570.	Reis, Leonardo, Luis Aguiar, Darío Baptista, and Fernando Morgado-Dias. "A software tool for automatic generation of neural hardware." Neuron 1, no. 1 (2014): 229-235.	4	2,00

44	Oniga Stefan, Tisan Alin, Mic Daniel, Buchman Attila, Vida-Ratiu Andrei, Hand postures recognition system using artificial neural networks implemented in FPGA, Electronics Technology, 30th International Spring Seminar on, Cluj-Napoca 2007. pp. 507-514	Zavala-Arriaza Manuel Leobardo, Valdez Fevrier, Melin Patricia, Architecture of Modular Neural Network in Pattern Recognition, In: Recent Advances on Hybrid Intelligent Systems. Springer, 2013. (ISBN 3642330207) pp. 211-219.	5	1,60
45	-//-	Tamee Kreangsak, Chaiwong Khomyuth, Yothapakdee Kriengsak, Yupapin Preecha P, Muscle sensor model using small scale optical device for pattern recognitions, The Scientific World Journal 2013: Paper 346047. 6 p. (2013)	5	1,60
46	-//-	Hikawa, Hiroomi, and Keishi Kaida. "Novel FPGA Implementation of Hand Sign Recognition System With SOM–Hebb Classifier." Circuits and Systems for Video Technology, IEEE Transactions on 25.1 (2015): 153-166.	5	1,60
47	-//-	Farouk Kheir Eldin, M. (2015). Principal component pyramids using image blurring for nonlinearity reduction in hand shape recognition (Doctoral dissertation, Dublin City University).	5	1,60
48	A Tisan, S Oniga, C Gavrincea, Hardware implementation of a MLP network with on-chip learning, In: WSEAS (szerk.), Proceedings of the 5th WSEAS Int. Conf. on DATA NETWORKS, COMMUNICATIONS & COMPUTERS. 2006	Ozdemir AT, Danisman K, Asyali MH, FPGA based arrhythmia classifier, In: Biomedical Engineering Meeting, 2009. BIYOMUT 2009. 14th National. IEEE, 2009. (ISBN 1424436052) pp. 1-4.	3	2,67
49	-//-	ÖZDEMİR, Ahmet Turan, and Kenan DANIŞMAN. "Fully parallel ANN-based arrhythmia classifier on a single-chip FPGA: FPAAC." Turkish Journal of Electrical Engineering and Computer Science 19.4 (2011): 667-687.	3	2,67
50	-//-	Laudani, A., Lozito, G. M., Fulginei, F. R., & Salvini, A. (2015). On training efficiency and computational costs of a feed forward neural network: a review. Computational intelligence and neuroscience, 2015, 83.	3	2,67
51	-//-	Pratap, Rana; Subadra, M, Efficient Digital Implementation of The Sigmoidal Function For Artificial Neural Network, Proceedings of International Conference on Light - Optics - Phenomena, Materials , Devices, and Characterization, Book Series: AIP Conference Proceedings Volume: 1391, Calicut, INDIA Date: MAY 23-25, 2011	3	2,67
52	A. Tisan and S. Oniga. "Current status of electronic nose. The sensing System International Multidisciplinary Conference 5th edition, Baia Mare, 23-24 May 2003." Scientific Bulletin, series C 17 (2003): pp. 517-522.	Ali, M. E., Kashif, M., Uddin, K., Hashim, U., Mustafa, S., & Man, Y. B. C. (2012). Species authentication methods in foods and feeds: the present, past, and future of halal forensics. Food Analytical Methods, 5(5), 935-955.	2	4,00
			Total	**119,96**

A3.1.2. Citari in reviste si volume ale unor manifestari stiintifice indexate in alte baze de date internationale (BDI)				
Nr.	**Articol citat**	**Articol care citeaza**	**Numar autori art.citat**	**Punctaj**
1	S. Oniga, J. Suto, Human activity recognition using neural networks, in Proceedings of the 2014 15th International Carpathian Control Conference, ICCC 2014, pp. 403-406, 2014.	Pal, Sandipan, and Charith Abhayaratne. "Video-based activity level recognition for assisted living using motion features." Proceedings of the 9th International Conference on Distributed Smart Camera. ACM, 2015.	2	2,00
2	-//-	C. Lung, S. Sabou, A. Buchman. "Modelling and implementation of intelligent sensor networks with applications in emergency situations management." Design and Technology in Electronic Packaging (SIITME), 2015 IEEE 21st International Symposium for IEEE, 2015.	2	2,00
3	-//-	Pal, Sandipan, Tian Feng, and Charith Abhayaratne. "Real-time recognition of activity levels for ambient assisted living." Consumer Electronics-Berlin (ICCE-Berlin), 2015 IEEE 5th International Conference on. IEEE, 2015.	2	2,00
4	I. Orha, S. Oniga, Study regarding the optimal sensors placement on the body for human activity recognition, 2014 IEEE 20th International Symposium for Design and Technology in Electronic Packaging (SIITME), pp, 203-206 2014.	S. Irene, N. M. Shwetha, P. Haribabu and R. Pitchiah, "Development of ZigBee Triaxial Accelerometer Based Human Activity Recognition System," Computer and Information Technology; Ubiquitous Computing and Communications; Dependable, Autonomic and Secure Computing; Pervasive Intelligence and Computing (CIT/IUCC/DASC/PICOM), 2015 IEEE International Conference on, Liverpool, 2015, pp. 1460-1466.	2	2,00
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			Total	**97,27**

A3.3.1. Membru in colectivele de redactie sau comitete stiintifice al revistelor, organizator de manifestari stiintifice, internationale indexate ISI				
Nr.	**Calitate (membru colectiv/comitet sau recenzor)**	**Revista/manifestare. ISSN**	**anul**	**Punctaj**
1	comitet	IEEE 15th International Carpathian Control Conference – ICCC 2014, Velke Karlovice, Czech Republic	2014	10
2	comitet	2013 IEEE 19th International Symposium for Design and Technology in Electronic Packaging (SIITME), Galati, Romania.	2013	10
3	comitet	International Conference on Hybrid Artificial Intelligence Systems – Special sesion: Metaheuristics for Combinatorial Optimization and Modelling Complex Systems, Salamanca, Spain	2013	10
4	comitet	IEEE 14th International Carpathian Control Conference – ICCC 2013, Kraków-Rytro, Poland	2013	10
			Total	**40**

A3.3.2. Membru in colectivele de redactie sau comitete stiintifice al revistelor, organizator de manifestari stiintifice, internationale indexate BDI				
Nr.	**Calitate (membru colectiv/comitet sau recenzor)**	**Revista/manifestare. ISSN**	**anul**	**Punctaj**
5	comitet	16th International Carpathian Control Conference – ICCC 2015, Szilvásvárad, Hungary, May 27-30, 2015	2016	6
6	comitet/recenzor 9 lucrari	Embedded world Conference Nuremberg, Germany	2016	6
7	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9689, Vol 8 nr.2	2015	6
8	recenzor	Infocommunications Journal, (publication of the Scientific Association for Infocommunications (Hungary), ISSN 2061-2079, (SCOPUS, INSPEC, COMPENDEX)	2015	6
9	comitet/recenzor 1 lucrare	10th International Conference on Soft Computing Models in Industrial and Environmental Applications BURGOS, SPAIN, 15th-17th June, 2015	2015	6
10	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9689, Vol 8 nr.1	2015	6
11	comitet/recenzor 8 articole	16th International Carpathian Control Conference – ICCC 2015, Szilvásvárad, Hungary, May 27-30, 2015	2015	6
12	comitet/recenzor 2 lucrari	The 5th International Conference on Recent Achievements in Mechatronics, Automation, Computer Sciences and Robotics, MACRo 2015, SAPIENTIA University, Faculty of Technical and Human Sciences, Tirgu Mures, Romania on 6-7th March, 2015.	2015	6
13	comitet/recenzor 8 articole	Embedded world Conference Nuremberg, Germany	2015	6

14	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9689	2014	6
15	comitet/recenzor 13 articole	Embedded world Conference Nuremberg, Germany	2014	6
16	comitet	International Review of Applied Sciences and Engineering	2010-2014	6
17	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9689	2013	6
18	comitet	IEEE 18th International Symposium for Design and Technology in Electronic Packaging (SIITME), Alba Iulia, Romania	2012	6
19	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9689	2012	6
20	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9689	2011	6
21	comitet	IEEE 17th International Symposium for Design and Technology in Electronic Packaging, Timisoara, Romania	2011	6
22	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9690	2010	6
23	comitet	16th International Symposium for Design and Technology in Electronic Packaging, September 23-26, 2010, Pitesti, Romania	2009	6
24	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9691	2009	6
25	comitet	IEEE 15th International Symposium for Design and Technology of Electronics Packages, 2009, Gyula, Hungary	2009	6
26	editor sef/recenzor	Carpathian Journal of Electronic and Computer Engineering, 1844 - 9692	2008	6
27	managing editor	Carpathian Journal of Electrical Engineering, ISSN 1843-7583	2007	6
28	comitet / chairman	Regional Conference on Embedded and Ambient Systems - RCEAS 2007 Budapesta, Ungaria	2007	6
29	comitet organizare/comitet stiintific	Embedded Systems Design and Apllications, ESDA 2007 Baia Mare, Romania	2007	6
30	comitet	4th International Workshop of ELECTROMAGNETIC COMPATIBILITY, CEM 2007 , Baia Mare, Romania	2007	6
			Total	**156**

A3.3.3. Membru in colectivele de redactie sau comitete stiintifice al revistelor, organizator de manifestari stiintifice, nationale/internationale neindexate				
Nr.	**Calitate (membru colectiv/comitet sau recenzor)**	**Revista/manifestare. ISSN**	**anul**	**Punctaj**
31	comitet	Conference on Embedded Systems and Wireless Sensors Networks Design and Applications, Debrecen Hungary	2012	3
32	comitet	Conference on Embedded Systems and Wireless Sensors Networks Design and Applications, Debrecen Hungary	2011	3
33	comitet	International Carpathian Control Conference ICCC'2010. Eger, Ungaria	2010	3
34	comitet	15th Building Services, Mechanical and Building, Industry Days, International Conference and Exhibition, Secţiunea Informatică aplicată, Debrecen, Hungary	2009-2011	3
35	comitet	14th International Symposium for Design and Technology of Electronics Packages, Predeal, Romania	2008	3
36	comitet organizare/comitet stiintific	13th International Symposium for Design and Technology of Electronics Packages, Baia Mare, Romania	2007	3
37	comitet	International Symposium on Applied Informatics and Related Areas. Székesfehérvár, Ungaria	2010	3
38	comitet organizare/comitet stiintific	Embedded Systems Design and Apllications, ESDA 2010. Baia Mare, Romania	2010	3
39	comitet organizare/comitet stiintific	Embedded Systems Design and Apllications, ESDA 2009. Baia Mare, Romania	2009	3
40	comitet organizare/comitet stiintific	Embedded Systems Design and Apllications, ESDA 2008. Baia Mare, Romania	2008	3
			Total	**30**

A3.4.1. Premii in domeniu: Academia Romana, ASTR, academii de ramura, premii internationale			
Nr.	Premiul	Anul	Punctaj
1	Medalia AGEPI (Medalia Agenției de stat pentru proprietate intelectuală, Republica Moldova) , Târgul Internațional de Invenții și Idei Practice "INVEST-INVENT SIR 21"	2012	15
2	"Faculty of Informatics Award Honours" for the outstanding academic and scientific achievements. University of Debrecen 2015	2016	15
		Total	**30**

A3.4.2. Premii in domeniu: nationale			
Nr.	Premiul		Punctaj
1	Best Adviser in the eleventh Edition of the Digilent Design Contest, EU Region, Cluj-Napoca România, 2014	2015	5
		Total	**5**